

**NOTICE OF APPEAL FROM THE EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

TI-35373

In re application of: Morio Nakao

Confirmation No.: 2030

Serial No.: 10/631,083

For: HIGH RELIABILITY CHIP SCALE PACKAGE

Filed: 07/31/2003

Examiner: David E. Graybill

Group Art Unit: 2822

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision in the Office Action dated 06/19/2007 of the Primary Examiner finally rejecting claims 18 & 19.

The item (s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
 ☐ was granted on
 ☐ is requested for _____ month(s).
2. ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☐ Fee \$500.00:
 ☒ Not required (*Fee paid in prior appeal*)
 ☒ The Commissioner is hereby requested to grant any time extension that may be due, and is authorized to charge any fees which may be required incident to this filing, or credit any overpayment to Deposit Account No. 20-0668.

Signature [Rule 191(b)]

Respectfully submitted,
/Yingsheng Tung/

Post Office Address
(to which correspondence is to be sent)

Yingsheng Tung
Reg. No. 52,305
Texas Instruments Incorporated
P.O. Box 655474, M.S. 3999
Dallas, TX 75265